

Patent/Publication: WO2002063818A1 ERROR CORRECTING/DECODING METHOD

Bibliographic

DWPI Title

Error correcting/decoding method

Original Title

ERROR CORRECTING/DECODING METHOD

Assignee

Standardized: MITSUBISHI ELECTRIC CORP ; UGA SHINSUKE

Original: MITSUBISHI DENKI KABUSHIKI KAISHA

Inventor

UGA Shinsuke

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2002-08-15 (A1)

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Abstract

Abstract

An error correcting/decoding method for a TFCI added to transmission data, transmitted along with the transmission data, and representing the transmission format of the transmission data, wherein the data judged by the receiving side that the occurrence probability of a transmission message is obviously zero is excluded from the object of comparison, and the data judged by the receiving side that the occurrence probability of the transmission message is obviously zero is not calculated, thereby improving the error correcting characteristics and reducing the amount of processing.

French Abstract

La présente invention concerne un procédé de correction d'erreur et de décodage portant sur un code TFCI (Transport Format Combination Indicator) ajouté à des données d'émission. Ce code, émis en même temps que les données d'émission, représente le format d'émission des données d'émission. En l'occurrence, on exclut de l'objet de comparaison les données pour lesquelles on considère, côté réception, que la probabilité d'existence d'un message d'émission est de toute évidence nulle. En outre, on ne fait aucun calcul pour les données pour lesquelles on considère, côté réception, que la probabilité d'existence d'un message d'émission est de toute évidence nulle, ce qui améliore les caractéristiques de correction d'erreurs et réduit la masse de traitement.

Classes/Indexing

IPC

IPC Code(1-7) **H04L 1/00** H03M 13/15 H04J 13/04

(7)

Current IPC-R	Invention	Version	Additional	Version
Advanced	G11B 20/18	20060101	-	-
	H03M 13/00	20060101		
	H03M 13/15	20060101		
	H03M 13/39	20060101		
	H04J 13/04	20060101		
	H04L 1/00	20060101		
Core	G11B 20/18	20060101	-	-
	H03M 13/00	20060101		
	H04J 13/02	20060101		
	H04L 1/00	20060101		
Subclass	-	-	-	-

ECLA

G11B002018 H03M001300 H03M001339 H04L000100B5L


Locarno Class

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Legal Status**INPADOC Legal Status**

Gazette Date	Code	Description
2005-05-31	WWW -	WIPO INFORMATION: WITHDRAWN IN NATIONAL OFFICE EP 2001902784
2003-03-12	WWP +	WIPO INFORMATION: PUBLISHED IN NATIONAL OFFICE EP 2001902784
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2002-08-15	AL +	DESIGNATED COUNTRIES FOR REGIONAL PATENTS WO 02063818 A1 AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LU; MC; NL; PT; SE
2002-03-19	ENP	ENTRY INTO THE NATIONAL PHASE IN: 2002 563647 A JP

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INPADOC Family (6)

Publication Number	Publication Date	Inventor	Assignee/Applicant	Title
WO2002063818A1	2002-08-15	UGA Shinsuke	mitsubishi ELECTRIC CORP	ERROR CORRECTING/DECODING METHOD
JP3914877B2	2007-05-16	-	mitsubishi ELECTRIC CORP	The error-correction decoding method
CN1422471A	2003-06-04	SHINSUKE UGA	mitsubishi ELECTRIC CORP	Error correcting/decoding method
EP1292057A4	2005-06-22	UGA SHINSUKE	mitsubishi ELECTRIC CORP	ERROR CORRECTING/DECODING METHOD
EP1292057A1	2003-03-12	UGA Shinsuke c/o mitsubishi DENKI KABUSHIKI K.	mitsubishi ELECTRIC CORP	ERROR CORRECTING/DECODING METHOD

US20030088819A1	2003-05-08	Uga Shinsuke	-	Error correction/decoding method
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Claims

No Claims exist for this Record

Description

Background/ Summary

 Expand Background/Summary

Drawing Description

 Expand Drawing Description

Description

Description

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Citations

Citations

Citing Patents (3)

Publication Number	Publication Date	Application Date	Assignee/Applicant-Original	Title
GB2392587A	2004-03-03	2003-08-28	NEC Corporation (JP)	Determination of transport format combination indicators at the end of data blocks and application to later blocks if the earlier block is error free
GB2392587B	2006-09-27	2003-08-28	-	Transport format (TF) determination apparatus, method and program, for use in wireless transmission
US7168015B2	2007-01-23	2003-09-03	NEC Corporation Tokyo JP	TF-determination apparatus, and TF-determination method as well as program to be executed for implementing the TF-determination method

Cited Patents (5)

Publication Number	Publication Date	Application Date	Inventor	Assignee/Applicant-Original	Title	Relevance	Source
JP10112130A	1998-04-28	1996-10-04	ITOI TETSUSHI	NEC CORP	DATA REPRODUCING DEVICE	X	0
JP2000516072A	-	-	-	-	-	-	-
JP2000516072W	-	-	-	-	-	-	-
JP61007730A	1986-01-14	1984-06-22	FURUYA YUKITSUNA	NEC CORP	ERROR CORRECTING DECODER	X	0
JP9074359A	1997-03-18	1995-09-04	TAJIMA KAZUYUKI	FUJITSU LTD	ERROR CORRECTION AND DECODING CIRCUIT	A	0

Cited Non-patents (2)

- 3GPP, "3GPP TS25.212 3rd generation partnership project; Technical specification group radio access network; Multiplexing and channel coding(FDD) (Release1999)", V3.4.0, October 2000, "4.3.3 coding of transport-format-combination indicator (TFCI)", <URL://www.3gpp. org/ftp/Specs/2000-09/ R1999/25_series/> pages 48-52, XP002938096
- See also references of EP 1292057A1

Other**Attorney-Agent**

FUKAMI, Hisao

Parent Case

Parent Case

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Designated StatesCN JP US , **European patent:** AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

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